



# VISVESVARAYA



## COLLEGE OF ENGINEERING & TECHNOLOGY

Approved by AICTE, New Delhi & Govt. of T.S. Accredited with NAAC 'A' Grade, Affiliated to JNTUH, Hyderabad  
Sponsored by : Jawahar Educational Society, An ISO 9001 : 2018 and ISO 14001 : 2015 Certified Institution

### Department of ECE

#### 5.7.1 Development Activities (10)

#### (A) Product Development

ACADEMIC YEAR 2023-2024

S.No	ROLLNO	NAME OF THE STUDENT	INTERNAL GUIDE	PROJECT TITLE
1	21BT5A0402	A.MANISHA	Dr.KVRAMPR ASAD	SATELLITE IMAGE ENHANCEMENTUSI NG 2D LEVEL
2	21BT5A0409	G.NIKITHA		
3	21BT5A0403	A.RAJKUMAR		
4	20BT1A0403	E.BHANUPRAKASH		
5	21BT5A0401	A.SOWMYA	Dr.HRAGHUP ATHI	RECOGNITIONOFVEH CLE NUMBER PLATE USING MAT
6	21BT5A0407	B.RAMYA KUMARI		
7	21BT5A0408	CH.VINAY CHARY		
8	21BT5A0418	M.SREEDHAR		
9	21BT5A0425	W.ROSHINI	H.SOMASHEKAR	DESIGNAND IMPLEMENTATION OF WOMENSAFETYSY ST EM USING BASED ON IOT TECHNOLOGY
10	21BT5A0419	N.RAMYA		
11	21BT5A0429	C.CHANDRAKIRAN		
12	21BT5A0406	B.VINAY		
13	21BT5A0410	G.THRIVENI	BSWETHA	IOTBASEDAIRQUAL ITY MONITORING SYSTEM
14	21BT5A0414	K.ASHWINI		
15	21BT5A0422	T.PAVAN		
16	20BT1A0401	A.VENU		
17	20BT1A0402	CH.SNEHA	B.SANTHOSHI	HOMEAUTOMATIO NBASD ON IOT
18	21BT5A0415	K.SAIMEGHANA		
19	21BT5A0417	M.NAVEENKUMAR		





# Visvesvaraya

## College of Engineering & Technology

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### Department of ECE

### ACADEMIC YEAR 2022 - 2023

S. No		ROLLNO	NAME OF THE STUDENT	INTERNAL GUIDE	PROJECT TITLE
1	BATCH 1	21BT5A0402	A.MANISHA	Dr. K V RAM PRASAD	CMOS PHASE & FREQUENCY DETECTOR
2		21BT5A0409	G.NIKITHA		
3		21BT5A0403	A.RAJKUMAR		
4		20BT1A0403	E.BHANUPRAKASH		
5	BATCH 2	21BT5A0401	A.SOWMYA	Dr. H RAGHUPATHI	VOLTAGE LEVEL SHIFTER
6		21BT5A0407	B.RAMYAKUMARI		
7		21BT5A0408	CH.VINAYCHARY		
8		21BT5A0418	M.SREEDHAR		
9	BATCH 3	21BT5A0425	W.ROSHINI	H.SOMASHEKAR	PSEUDO RANDOM NUMBER GENERATOR IN FPGA AND CMOS VLSI
10		21BT5A0419	N.RAMYA		
11		21BT5A0429	C.CHANDRAKIRAN		
12		21BT5A0406	B.VINAY		
13	BATCH 4	21BT5A0410	G.THRIVENI	BSWETHA	4-BIT CSA USING NMOS
14		21BT5A0414	K.ASHWINI		
15		21BT5A0422	T.PAVAN		
16		20BT1A0401	A.VENU		
17	BATCH 5	20BT1A0402	CH.SNEHA	B.SANTHOSHI	DESIGN OF POWER EFFICIENT 14T-FULL ADDER CIRCUIT
18		21BT5A0415	K.SAIMEGHANA		
19		21BT5A0417	M.NAVEENKUMAR		
20		21BT5A0404	KRISHNAGOUTHAM		
21	BATCH 6	20BT1A0406	L.RISITHA	P.VENKATRAMULU	DESIGN OPTIMIZATION TECHNIQUES LOW POWER VLSI
22		20BT1A0405	J.VAMSHI		
23		21BT5A0423	T.HARISH		
24		21BT5A0420	P.RAJESH		
25	BATCH 7	20BT1A0408	S.SADHANA	KAKSHARA	CMOS 1-BIT COMPARATOR USING STACKING TECHNIQUE
26		21BT5A0411	J.PRANEETHA		
27		21BT5A0421	R.KUMARASWAMY		

28		21BT5A0424	V.AKHILCHARY	BATCH 8	N LAXMI	LOW POWER HALFRATE PRBS GENERATOR
29		20BT1A0409	B.KIRAN			
30		20BT1A0404	G.SAIKUMAR			
31		21BT5A0405	B.RAKESH			
32		21BT5A0413	K.PAVANKALYAN			
33		21BT5A0428	D.HARSHAVARDHAN			

Sl. No	ROLL NO	NAME OF THE STUDENT	INTERNAL GUIDE	PROJECT TITLE
1	21BT5A0402	A.MANISHA	Dr. K V RAM PRASAD	CMOS PHASE FREQUENCY DETECTOR
2	21BT5A0409	G.NIKITHA		
3	21BT5A0403	A.RAJKUMAR		
4	20BT1A0403	E.BHANUPRASH		
5	21BT5A0401	A.SOWMYA	Dr. H RAGHUPATHI	VOLTAGE LEVEL SHIFTER
6	21BT5A0407	B.RAMYAKUMARI		
7	21BT5A0408	CH.VINAYACHARY		
8	21BT5A0418	M.SREEDHAR		
9	21BT5A0425	W.ROSHINI	H.SOMASHEKAR	PSEUDO RANDOM NUMBER GENERATOR IN FPGA AND CMOS VLSI
10	21BT5A0419	N.RAMYA		
11	21BT5A0429	C.CHANDRAKRAN		
12	21BT5A0406	B.VINAY		
13	21BT5A0410	G.THRIVANI	BSWETHA	4-BIT SAUSING IMOS
14	21BT5A0414	K.ASHWINI		
15	21BT5A0423	T.PAVAN		
16	20BT1A0401	A.VENU		
17	20BT1A0402	CH.SNEHA	B.SANTHOSHI	DESIGN OF POWER EFFICIENT 14-BIT FULL ADDER CIRCUIT
18	21BT5A0415	K.SAIMEGHANA		
19	21BT5A0417	M.NAVEENKUMAR		
20	21BT5A0404	KRISHNAGOUTHAM		
21	20BT1A0405	L.RISITHA	P.VENKATRAMULU	DESIGN OPTIMIZATION TECHNIQUES LOW POWER VLSI
22	20BT1A0402	J.VAMSHI		
23	21BT5A0423	T.HARISH		
24	21BT5A0420	P.RAJESH		
25	20BT1A0408	S.SADHANA	KAKSHARA	CMOS 1-BIT COMPARATOR USING STACKING TECHNIQUE
26	21BT5A0411	J.PRANEETHA		
27	21BT5A0421	R.KUMARASWAMY		



Dept. of ECE

ACADEMIC YEAR 2021 - 2022

S.No	ROLLNO	NAME OF THE STUDENT	PROJECTGUIDE	PROJECT TITLE
1	19BT1A0405	B.UDAYKUMARGOUD	H.RAGHUPATHI	INTELLIGENT CARGO MANAGEMENT SYSTEM USING IOT
2	18BT1A0415	SAIVINIL		
3	20BT5A0402	CH.APARNA		
4	20BT5A0403	C.SAIKIRANGOUD		
5	20BT5A0409	K.KALYAN		
BATCH 1				
6	19BT1A0406	E.KAVYAREDDY	B.SANTHOSHI	SMART AGRICULTURE SYSTEM FOR FIELD PROTECTION AND MONITORING
7	19BT1A0411	S.THARUNKUMARGOUD		
8	20BT5A0412	N.VENUGOPAL		
9	20BT5A0419	SK.ABDULMUNNEB		
BATCH 2				
10	19BT1A0407	G.BHARATHSIMHA REDDY	B.SWETHA	IOT BASED HOME AUTOMATION USING TELEGRAM
11	20BT5A0414	U.LOKESHWARI		
12	20BT5A0416	Y.HARIPHAL		
13	20BT5A0408	K.HARSHAVARDHAN		
BATCH 3				
14	19BT1A0409	G.SAIKUMAR	K.V.RAMPRASAD	WEATHER BROADCAST SYSTEM USING IOT AND CLOUD
15	18BT1A0420	S.VINAY		

16	BATCH 4	19BT1A0402	A.SHIVASHANKAR		
17		20BT5A0404	G.EKAMBARAM		
19		19BT1A0410	M.PRANAVASREE	N.LAXMI	COALMINE WORKER SAFETYSYSTEM USINGCLOUD
20		19BT1A0401	A.MANISHA		
21	BATCH 5	18BT1A0417	P.LOHITHREDDY		
22		20BT5A0410	M.SAINIKHIL		
23		20BT5A0405	I.SRINU	P.VENKATARAM ULU	IOT BASED DRIVER DROWSINESS ALERTING SYSTEM
24		19BT1A0403	B.SAISWETHA		
25	BATCH 6	19BT1A0404	B.SAIRAM		
26		20BT5A0411	N.SRAVANI		
27		20BT5A0407	K.RADHIKA	H.SOMASHEKAR	IOT BASED WATER QUALITY MONITORING SYSTEM
28		19BT1A0408	G.NIKHILSAGAR		
29	BATCH 7	20BT5A0413	N.ARUN		
30		20BT5A0418	N.UMESHBABU		
31		20BT5A0417	R.SAIKIRANREDDY	N.LAXMI	VEHICLETHEFT AND AUTO ENGINE LOCKING SYSTEM USING IOT
32		19BT1A0412	S.UPPENDERRAO		
33	BATCH 8	20BT5A0401	A.CHANDRASHEKAR		
34		20BT5A0406	J.ANJEYULUYU		

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### ACADEMIC YEAR 2020 - 2021

S.No	BATCH NO	ROLL NO	NAME OF THE STUDENT	PROJECT TITLE	INTERNAL GUIDE
1	BATCH 1	18BT1A0416	N.MAHALAKSHMI	DESIGN OF A 4-BIT CARRY SELECT ADDER USING PASS NMOS TRANSISTOR	H.RAGHUPATHI
2		18BT1A0413	M.MAMATHA		
3		19BT5A0402	B.RENUKA		
4		17BT1A0402	B. LAHARI		
5		19BT5A0423	S.BHASKAR		
6	BATCH 2	19BT5A0409	G. PAVANI	LOW COST SORTING NETWORK USING UNARY PROCESSING	B.SANTHOSHI
7		19BT5A0401	A. ARAVIND		
8		18BT1A0411	K. ANUSHA		
9		19BT5A0410	G.PRANAYKUMAR		
10		18BT5A0403	E SANJEEVA		
11	BATCH 3	19BT5A0414	M. SRIJA	SERIAL PERIPHERAL INTERFACE TO INTEGRATED CIRCUIT USING VERILOG	N.LAXMI
12		18BT1A0404	D. ESHWAR		
13		19BT5A0404	E. BHUSHAN		
14		19BT5A0406	G. SANGEETHA		
15		18BT1A0410	K. KAVERI		
16	BATCH 4	19BT5A0408	G. NISHITHA	DESIGN OF LOW POWER AND HIGH SPEED COMPARATOR USING MUX BASED FULL ADDER CELL FOR MOBILE COMMUNICATIONS	K.RAMESH
17		18BT5A0401	A.SAIBHARATH		
18		19BT5A0403	B.CHINNA		
19		18BT1A0402	A.SAIKUMAR		
20		17BT1A0424	P. MALAVIKA		
21	BATCH 5	19BT5A0422	R.LAYA	PARITY PRESERVING ADDER OR SUBTRACTOR USING REVERSIBLE	H.SOMASHEKAR
22		19BT5A0417	N.SRAVANI		
23		19BT5A0405	G.PRADEEPREDDY		
24		19BT5A0413	M.PRAVALIKA		
25		19BT5A0413	KAVERI		
26	BATCH 6	18BT1A0412	K.BALAJI	DESIGN OF SINGLE-EVENT TOLERANT	H.SOMASHEKAR
27		18BT1A0408	K.ABHINASH REDDY		

